



Frequency Synthesis by Phase-Lock

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An engineering-level introduction to the fundamentals of frequency synthesis, with emphasis on phase-lock circuits. Discussions of the basic frequency manipulations and a detailed description of the phase-lock principle provide the necessary background for the subsequent detailed treatment of frequency synthesizer circuit design. Topics include loop response, pull-in, false locks, acquisition aids, computer simulation (CAD), fractional-N and related techniques, frequency dividers, phase detectors (including sample-and-hold), effects of sampling on loop stability, effects of contaminating waveforms in system components, and spectral purity. Includes 40 examples, 70 end-of-chapter problems, and numerous references.

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